

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: <b>Bruce B. Doris et al.</b>	Date: May 20, 2008
Serial Number: <b>10/536,483</b>	Examiner: <b>Minh-Loan T. Tran</b>
Filed: <b>05/24/2005</b>	Group Art Unit: <b>2826</b>
Title: <b>Strained FinFET CMOS Device Structure</b>	IBM Corporation D/18G, B/321, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

**LETTER**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

On May 5, 2008, Applicant paid the Issue Fee (See attached form).

On May 6, 2008 Applicant received via U.S. Postal Service Mail the attached Office Communication, (with Examiner's Amendment) dated May 1, 2008.

The Examiner's Amendment (on page 2) contains a spelling/typographical error. The word "step" is incorrect. The correct word is "stop", so that the insertion to Claim 1 should read -- , and further wherein an etch stop layer is disposed under the compressive layer and tensile layer.--

Applicant respectfully requests correction of the spelling/typographical error.

If necessary, please consider this Letter as a Petition under 35 USC 1.181, 1.182 and/or 1.183, as appropriate, and charge deposit account number 090458 for any fee. If a

fee is charged, then Applicant respectfully requests a credit of such fee to the Deposit Account because Applicant believes the error is an office mistake.

Respectfully Submitted,  
Bruce B. Doris, et al.

By: 

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